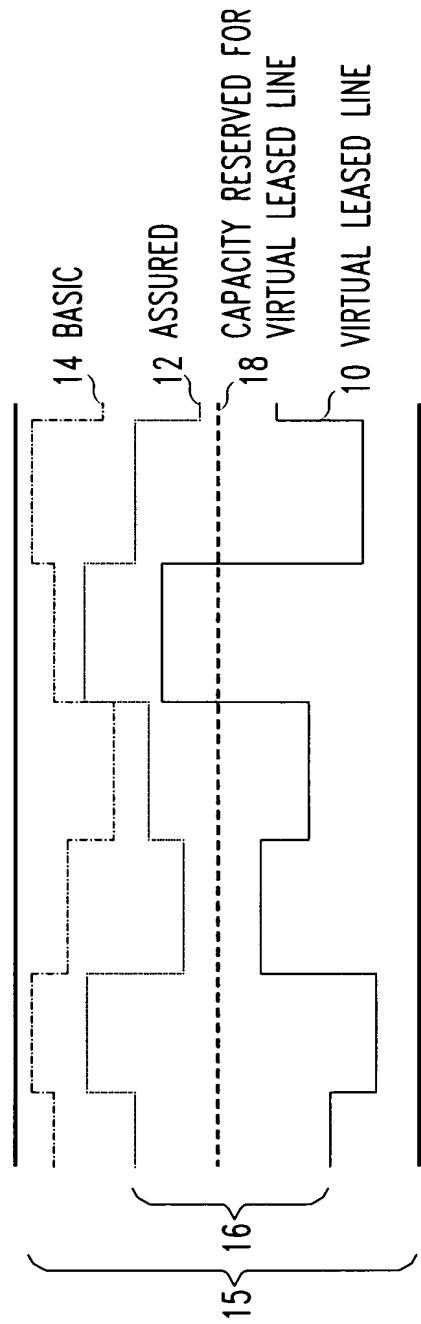


FIG. 1
PRIOR ART



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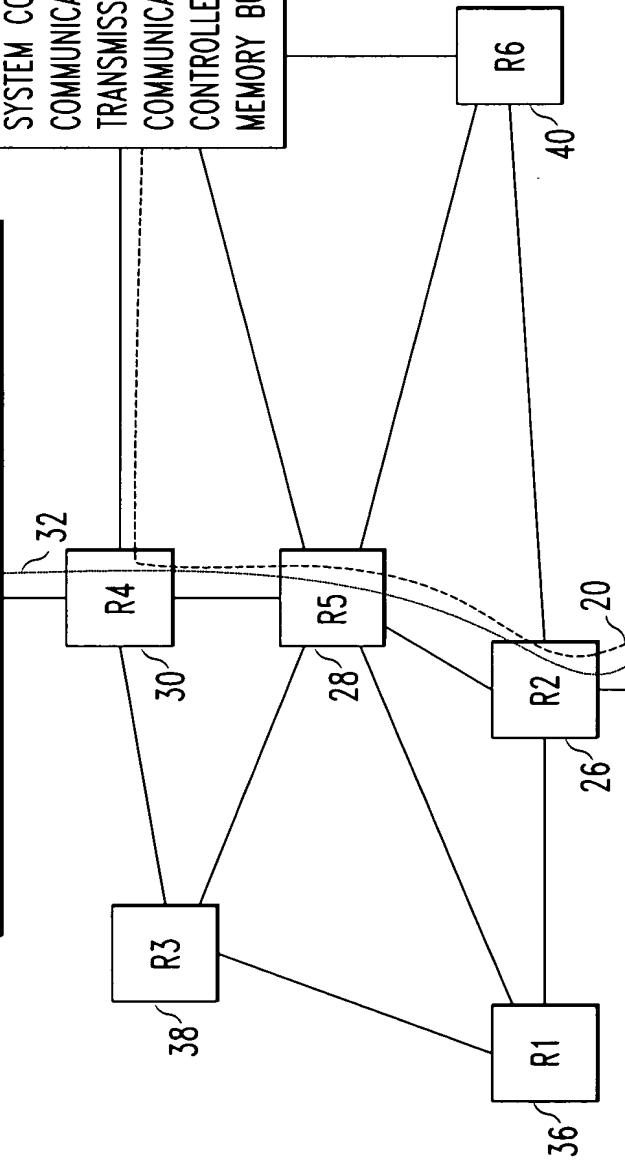
FIG. 2

PRIOR ART

34 VOICE GW2
 SYSTEM COMPRISING PROCESSOR, MEMORY,
 COMMUNICATION NETWORK CONNECTION, PACKET
 TRANSMISSION DELIVERY MECHANISM, COMMUNICATION
 NETWORK SENSING MECHANISM, CONTROLLER,
 BANDWIDTH ALLOCATION DEVICE, MEMORY BUFFER

24

VOICE GW3
 SYSTEM COMPRISING PROCESSOR, MEMORY,
 COMMUNICATION NETWORK CONNECTION, PACKET
 TRANSMISSION DELIVERY MECHANISM,
 COMMUNICATION NETWORK SENSING MECHANISM,
 CONTROLLER, BANDWIDTH ALLOCATION DEVICE,
 MEMORY BUFFER



22 VOICE GW1
 SYSTEM COMPRISING PROCESSOR, MEMORY, ~44
 COMMUNICATION NETWORK CONNECTION, PACKET ~48
 TRANSMISSION DELIVERY MECHANISM, COMMUNICATION ~50
 NETWORK SENSING MECHANISM, CONTROLLER, BANDWIDTH
 ALLOCATION DEVICE, MEMORY BUFFER ~54 ~51

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 52

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FIG. 3

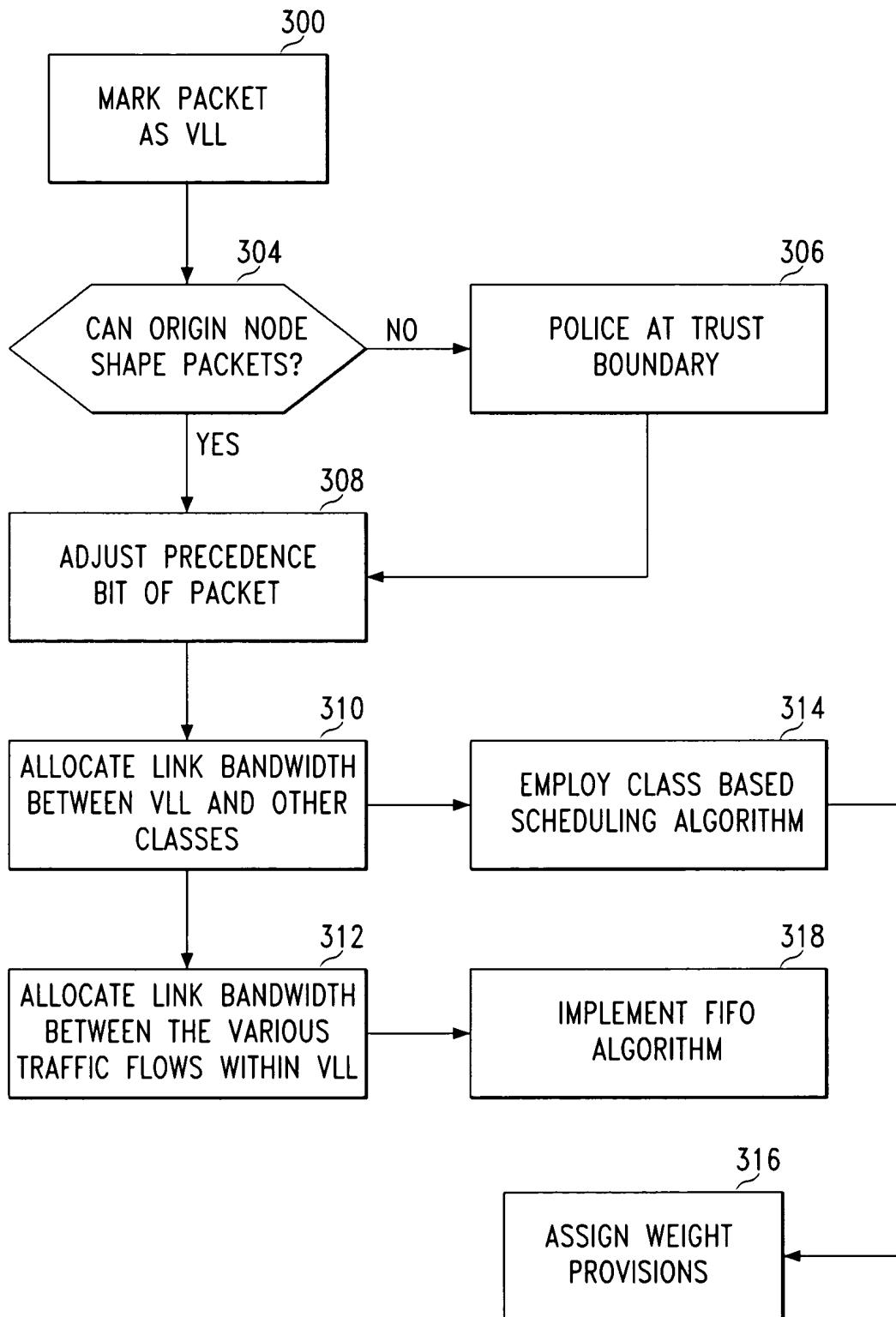


FIG. 4

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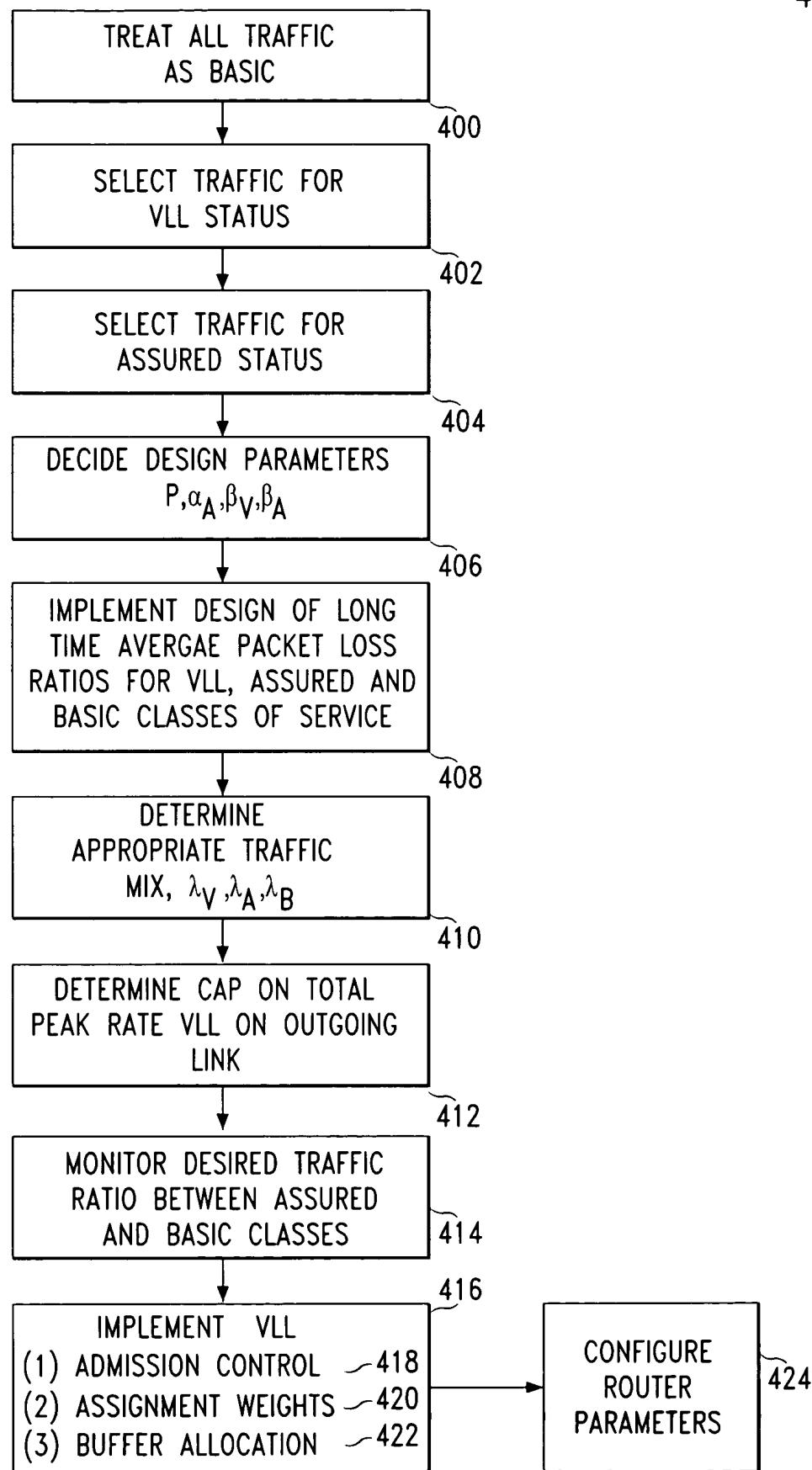
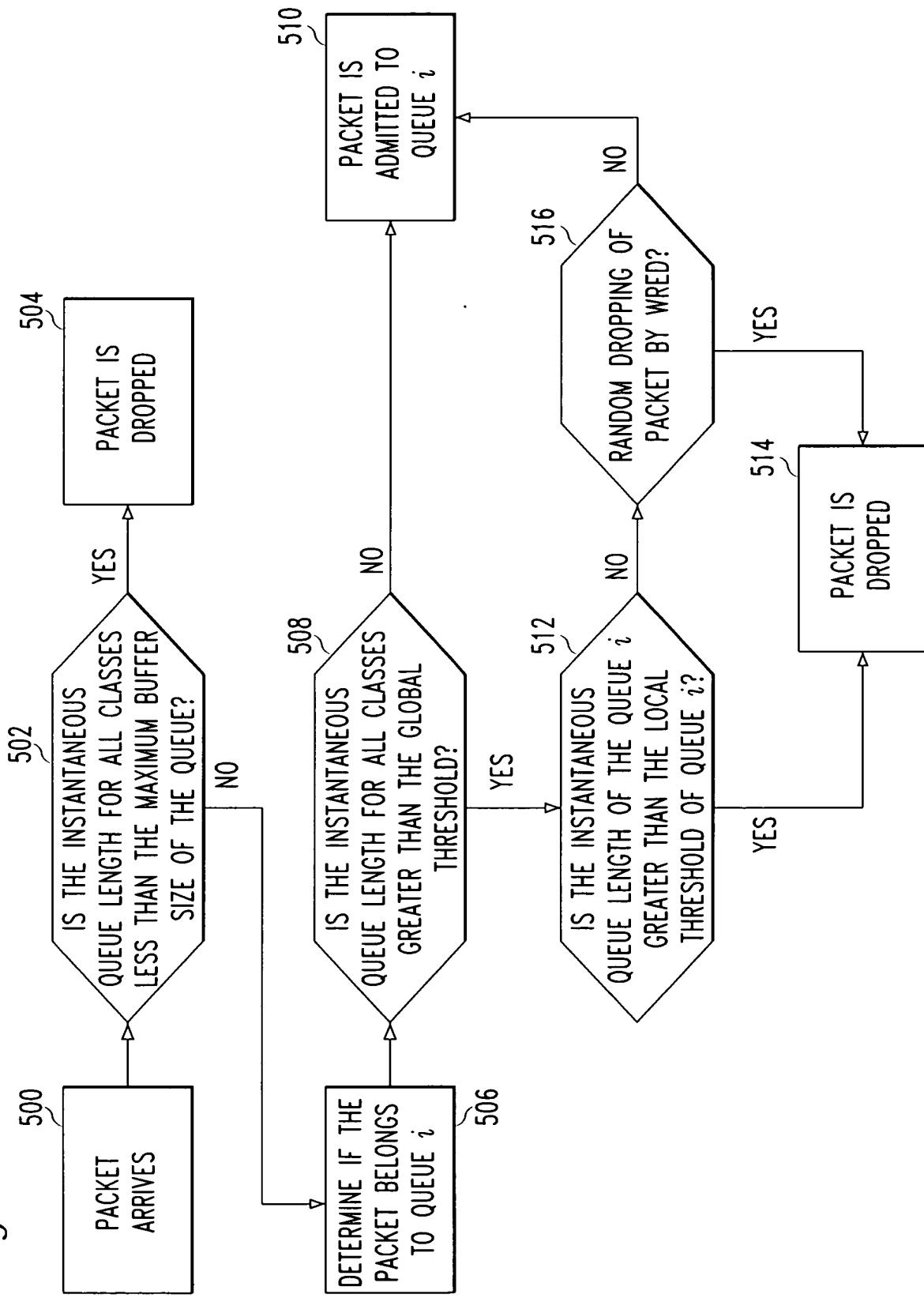


FIG. 5



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FIG. 6

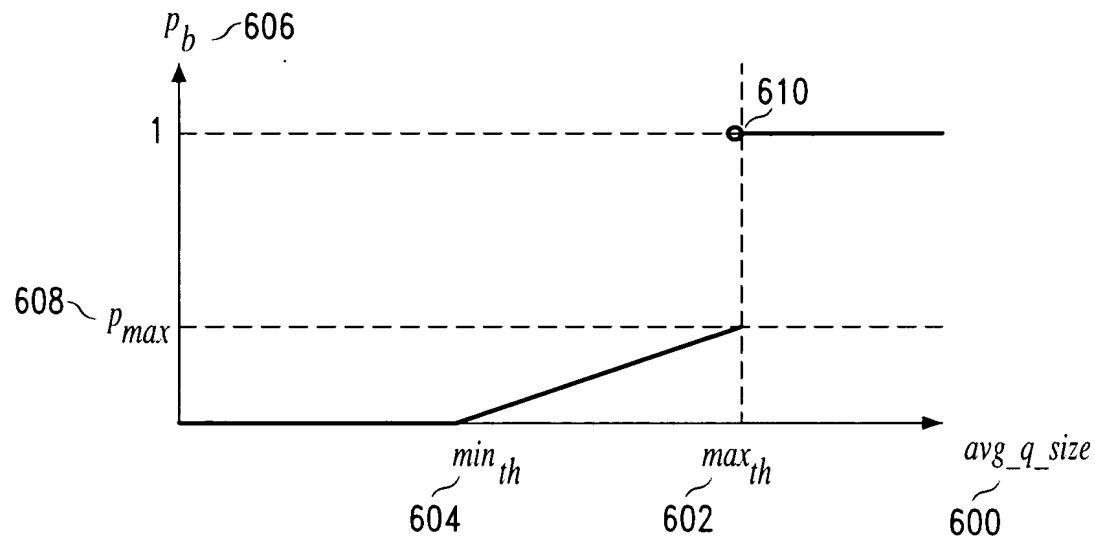
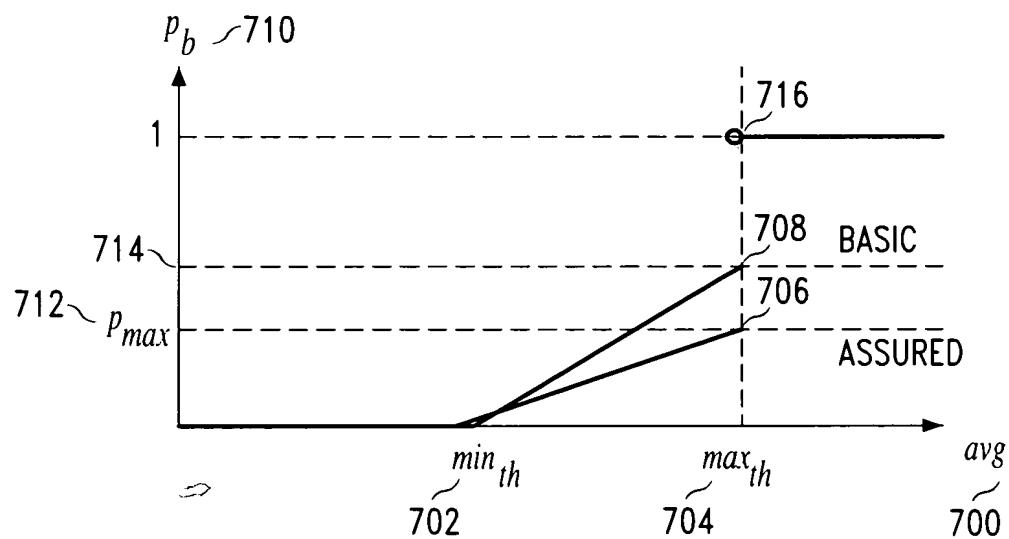
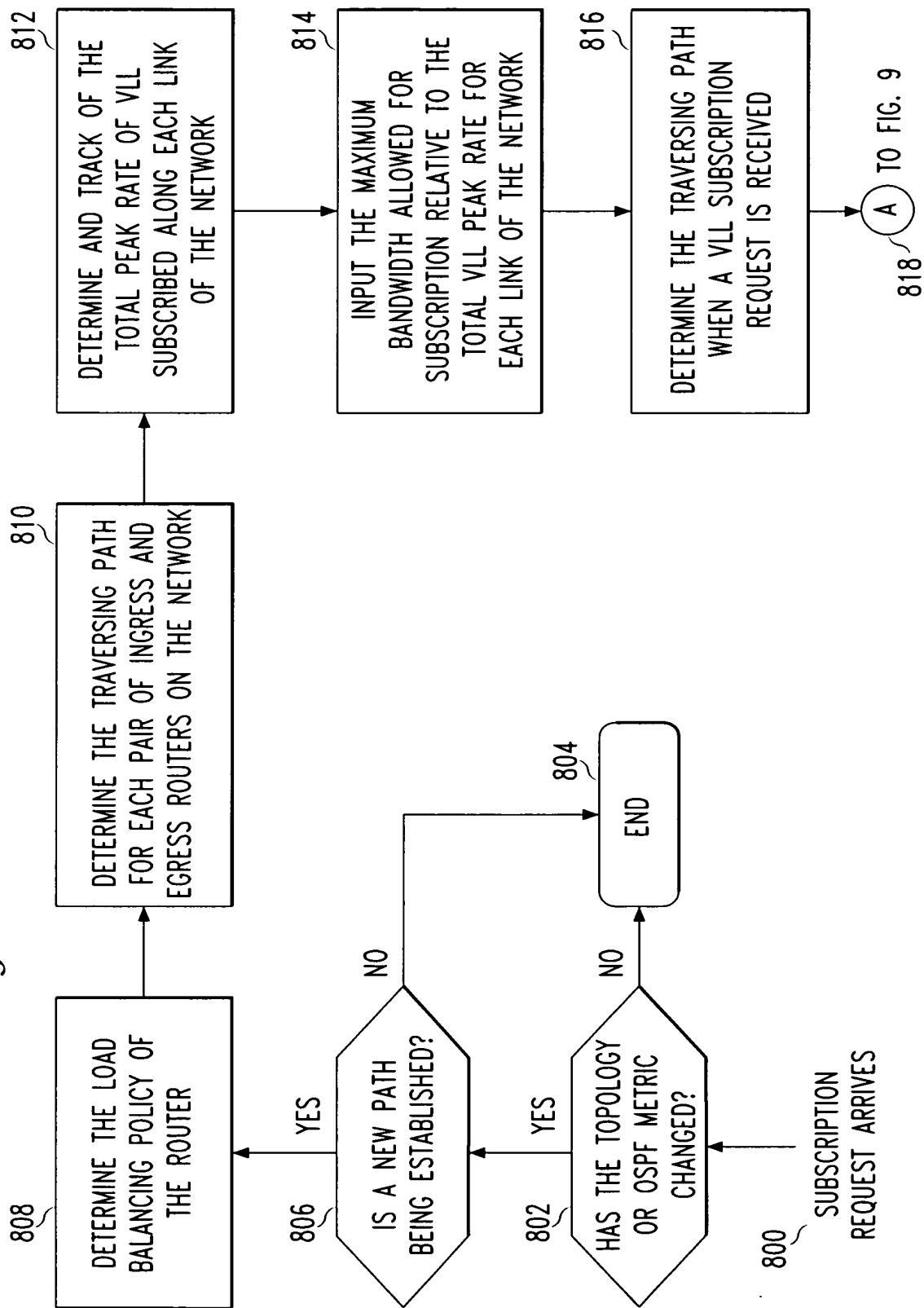


FIG. 7



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FIG. 8



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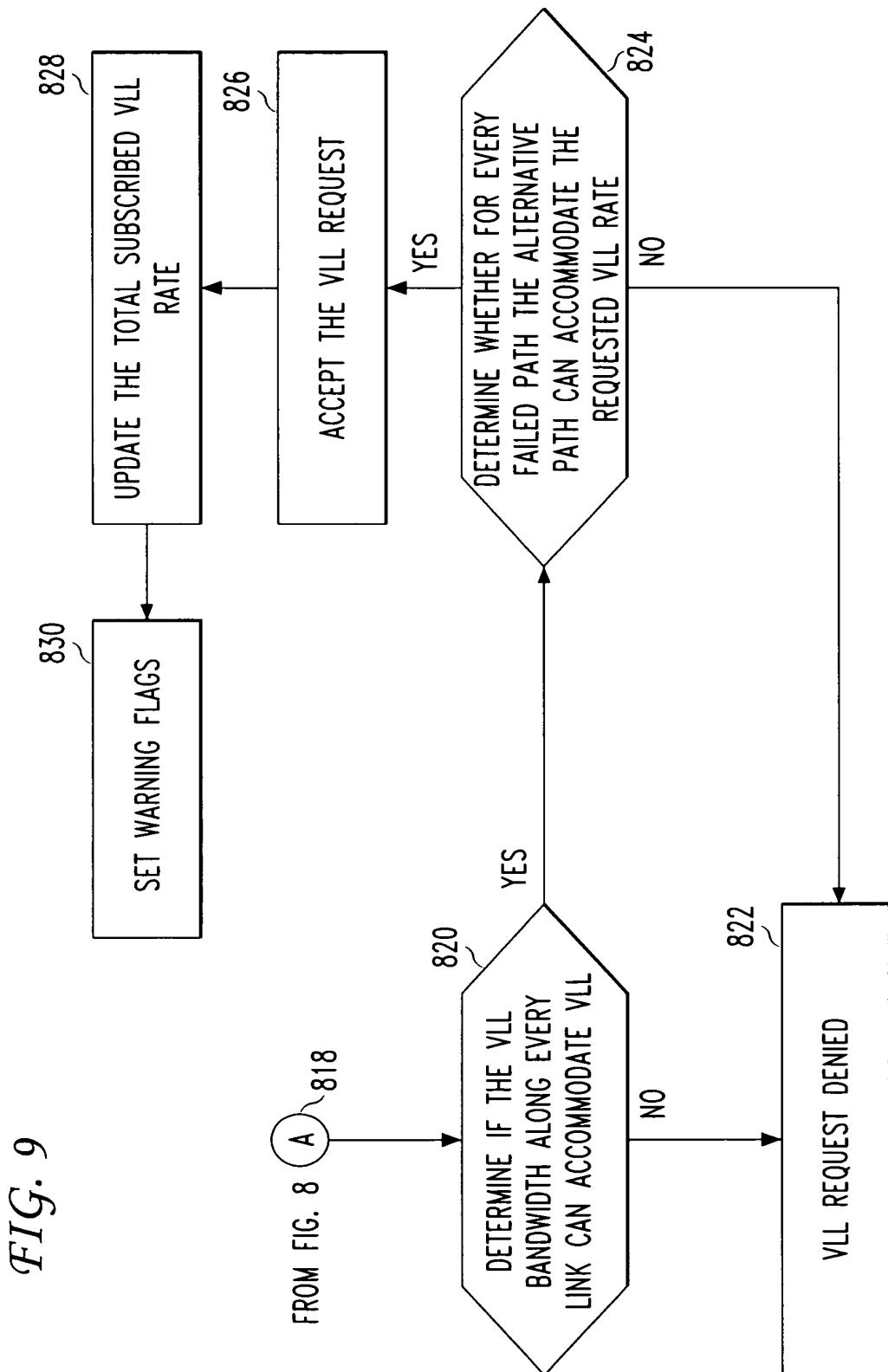


FIG. 9

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FIG. 10

